

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10074722	FILING DATE 02/11/2002	CLASS 438	SUBCLASS	GAU 2812	EXAMINER <i>RJ</i>
----------------------	---------------------------	--------------	----------	-------------	-----------------------

****APPLICANTS:** Pomarede Christophe; Givens Michael; Shero Eric; Todd Michael;

RJ Yes

****CONTINUING DATA VERIFIED:**

THIS APPLN CLAIMS BENEFIT OF 60/268,337 02/12/2001
AND CLAIMS BENEFIT OF 60/279,256 03/27/2001
AND CLAIMS BENEFIT OF 60/332,696 11/13/2001

RJ Yes

**** FOREIGN APPLICATIONS VERIFIED:**

RJ None

PG-PUB	<input type="checkbox"/> DO NOT PUBLISH	RESCIND	<input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes	<input type="checkbox"/> no
Verified and Acknowledged Examiner's initials		<input type="checkbox"/> yes	<input type="checkbox"/> no
ATTORNEY DOCKET NO ASME 1.320 A			
TITLE : Integration of high k gate dielectric			

STANLEY DEPARTMENT OF THE PATENT OFFICE, REV. 1-4

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Fige.Drwg.	Print Fig.
TERMINAL DISCLAIMER		Primary Examiner		
		Application Examiner		
PREPARED FOR ISSUE WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH:

DISK (CRF)

CD-ROM
(Attached in pocket on right inside flap)